RAA214401

150mA Wide Input Range Ultra Low Quiescent Current Linear Regulator

The RAA214401 is a linear regulator that operates with a wide input voltage range at a fixed output at 3.3V and up to 150mA of output current. With an ultra low quiescent current, it is suitable for always-on and keep-alive applications.

The device operates with a wide input voltage range (4.5V to 40V at 1mA load) with excellent line and load regulation. It has integrated fault protections such as Over-Temperature Shutdown (OTSD) and short-circuit current limit.

The output voltage of the device is well regulated across temperature and the entire operating range of the input voltage and load. It is fixed with an internal resistor network between the output pin and ground.

The RAA214401 is available in a SOT23 package.

Features

- Wide input voltage range (4.5V to 40V at 1mA load)
- Ultra low 3.6µA quiescent current
- High output voltage accuracy
- Excellent line and load regulation
- Stable with 2.2µF - 200µF MLCC output capacitor
- Integrated fault protections including thermal shutdown and current limit
- Available in the compact and cost-effective SOT23 package

Applications

- Always-on battery-powered equipment
- MCU standby power supply
- Electric meters
- Laptop computers and tablets
- Portable modules and appliances

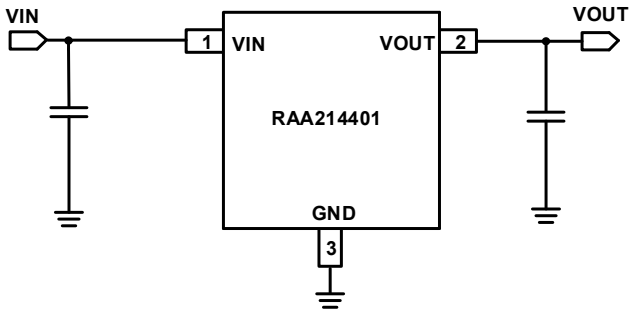


Figure 1. RAA214401 Typical Application

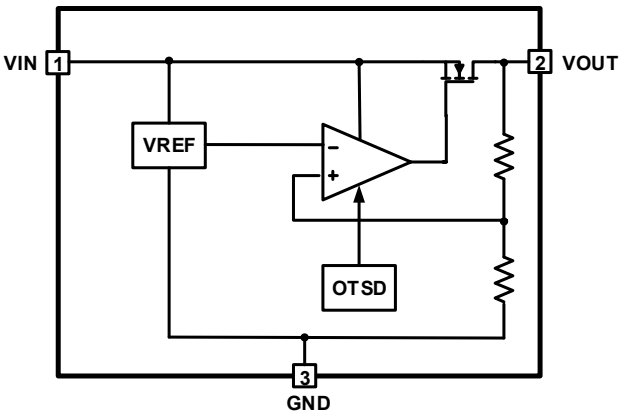
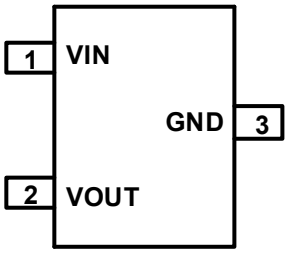


Figure 2. RAA214401 Block Diagram

Contents

1. Pin Information

1.1 Pin Assignments



Top View

1.2 Pin Descriptions

Pin Name	Pin Number	Description
VIN	1	Analog input supply voltage and positive supply for the linear regulator. Decouple this pin to ground with a 0.1µF or larger high frequency ceramic capacitor to GND.
VOUT	2	Regulated output voltage pin. An output MLCC capacitor is needed between this pin and GND. Place the capacitor as close to the output of the regulator as possible.
GND	3	Ground reference.

2. Specifications

2.1 Absolute Maximum Ratings

CAUTION: Do not operate at or near the maximum ratings listed for extended periods of time. Exposure to such conditions can adversely impact product reliability and result in failures not covered by warranty.

Parameter[1]	Minimum	Maximum	Unit
Supply Voltage, VIN	-0.3	+45	V
Output Voltage, VOUT	-0.3	6	V
Output Current, IOUT		Internally Limited	
ESD Ratings	**Value**		**Unit**
Human Body Model (Tested per JS-001-2017)	2		kV
Charged Device Model (Tested per JS-002-2014)	750		V
Latch-Up (Tested per JESD78E; Class 2, Level A)	100		mA

1. All voltages referenced to VSS unless otherwise specified.

2.2 Thermal Information

Thermal Resistance (Typical)[1]	θ_{JA} (°C/W)[2]	θ_{JC} (°C/W)[3]
SOT23	275	120

1. Specified at published junction-to-ambient thermal resistance for a junction temperature of +150°C. See Theta JA note for test condition to establish junction-to-ambient thermal resistance.
2. θ_{JA} is measured with the component mounted on a high-effective thermal conductivity test board in free air. See TB379.
3. For θ_{JC}, the case temperature location is on the top of the package.

Parameter	Minimum	Maximum	Unit
Maximum Junction Temperature	-65	+150	°C
Maximum Storage Temperature Range	-55	+150	°C
Pb-Free Reflow Profile	See TB493		

2.3 Recommended Operating Conditions

Parameter[1]	Minimum	Maximum	Unit
Supply Voltage, V_{DD}	+4.9 [2]	+40	V
Output Capacitor, C_{OUT}	2.2	200	uF
Junction Temperature	-40°C	+125	°C

1. All voltages referenced to VSS unless otherwise specified.
2. Minimum supply voltage at 150mA load. See Figure 19 for the minimum supply voltage at other loads.

2.4 Electrical Specifications

At internal junction temperature -40°C to +125°C, V_{IN} = 6V; C_{IN} = 1µF; C_{OUT} = 2.2µF, unless otherwise specified. Typical values are at T_A = 25°C.

Parameters	Symbol	Test Conditions	Min[1]	Typ	Max[1]	Unit
Input Voltage	V_{IN}	I_{OUT} = 1mA	4.5		40	V
		I_{OUT} = 150mA	4.9		40	
Output Voltage	V_{OUT}		3.20	3.30	3.39	V
Ground Current	I_{GND}	V_{IN} = 6V, I_{OUT} = 0		3.6	5.5	µA
		V_{IN} = 40V, I_{OUT} = 0			6.1	µA
		V_{IN} = 6V, I_{OUT} = 100mA		48		µA
		V_{IN} = 6V, I_{OUT} = 150mA		51		µA
Line Regulation	DV_{OUT}	4.5V ≤ V_{IN} ≤ 40V, I_{OUT} = 1mA		0.0003	0.005	%/V
Load Regulation	DV_{OUT}	0.1mA to 150mA		0.0025	0.006	%/mA
Dropout Voltage[2]	V_{DO}	I_{OUT} = 10mA		0.96	1.32	V
		I_{OUT} = 20mA		1.02	1.35	V
		I_{OUT} = 100mA		1.27	1.53	V
		I_{OUT} = 150mA		1.37	1.6	V
Output Voltage Temperature Coefficient		4.5V ≤ V_{IN} ≤ 40V, I_{OUT} = 1mA		32		ppm/°C
Output Voltage Noise	V_n	10Hz to 100kHz, I_{OUT} = 10mA		237		μV_{RMS}
Power Supply Ripple Rejection Ratio	PSRR	10Hz, I_{OUT} = 1mA		77		dB
		100Hz, I_{OUT} = 1mA		74		dB
		1kHz, I_{OUT} = 1mA		52		dB
		10kHz, I_{OUT} = 1mA		42		dB
Startup Time	t_{STR}	Time from V_{DD} = 5.4V to V_{OUT} = 0.95 * V_{OUT_NOM}, I_{OUT} = 0	370	470	670	µs
Short-Circuit Current Limit	I_{LIM}	V_{OUT} = 1V	150			mA
Thermal Shutdown		Temperature rising		155		°C
Hysteresis				17.5		°C

1. Parameters with MIN and/or MAX limits are 100% tested at +25°C, unless otherwise specified. Temperature limits established by characterization and are not production tested.
2. V_{DO} is measured at V_{OUT} = 0.98 x $V_{OUT(nom)}$

3. Typical Performance Graphs

Unless otherwise specified, operating conditions are at: T = +25°C; V_{IN} = 6V, C_{OUT} = 2.2µF

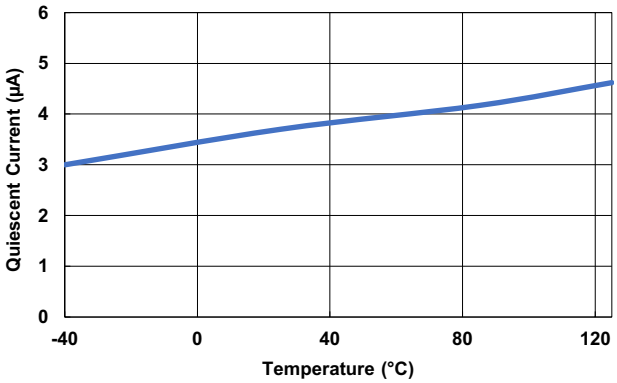


Figure 3. Quiescent Current (No Load) vs Temperature

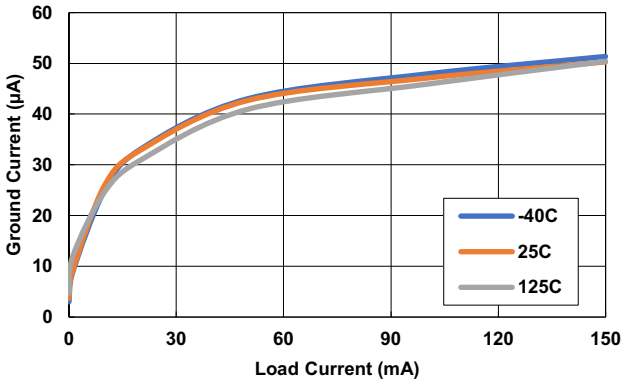


Figure 4. Ground Current vs Load

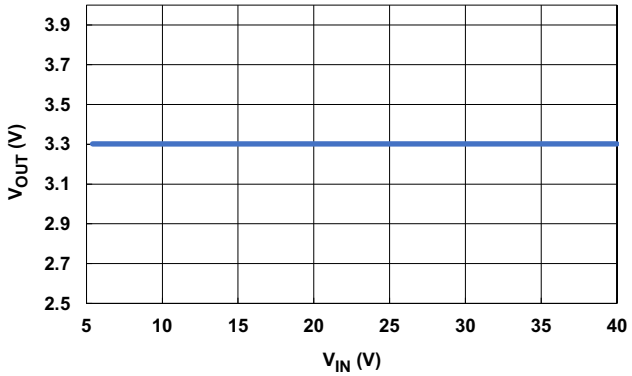


Figure 5. Output Voltage vs Input Voltage (I_{OUT} = 1mA)

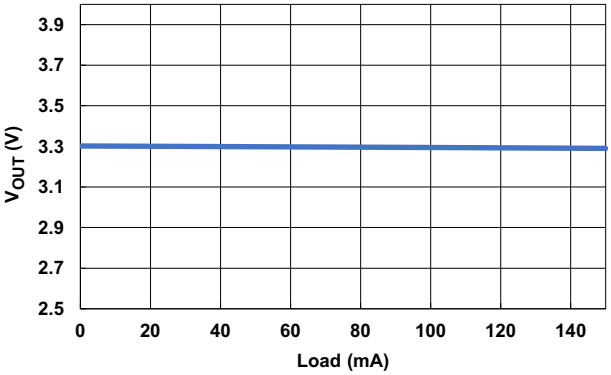


Figure 6. Output Voltage vs Load

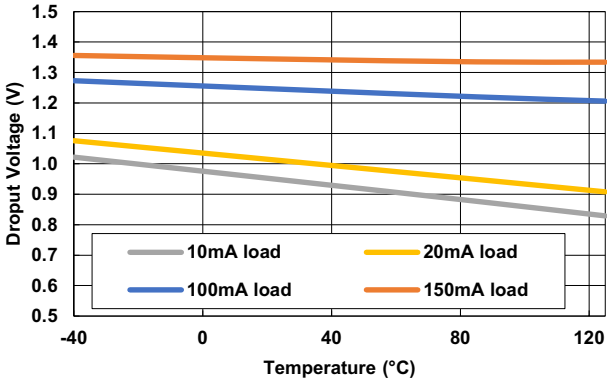


Figure 7. Dropout Voltage vs Temperature

Unless otherwise specified, operating conditions are at: T = +25°C; V_{IN} = 6V, C_{OUT} = 2.2µF (Cont.)

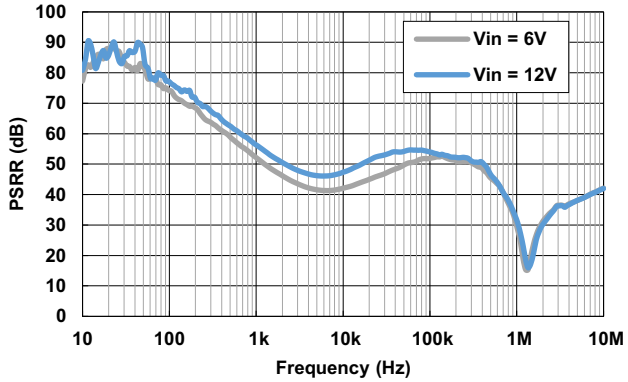


Figure 8. Power Supply Rejection Ratio (I_{OUT} = 1mA)

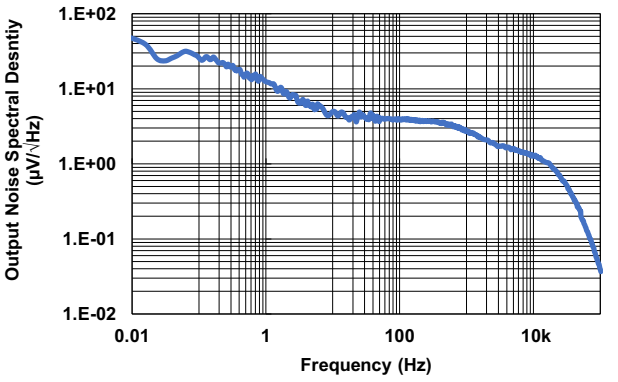


Figure 9. Output Noise Spectral Density (I_{OUT} = 10mA)

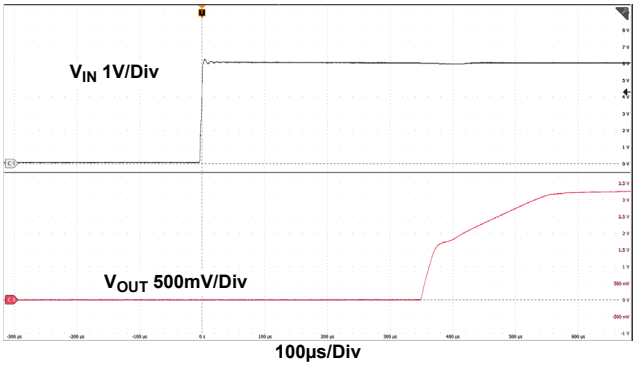


Figure 10. Startup Waveform (I_{OUT} = 10mA)

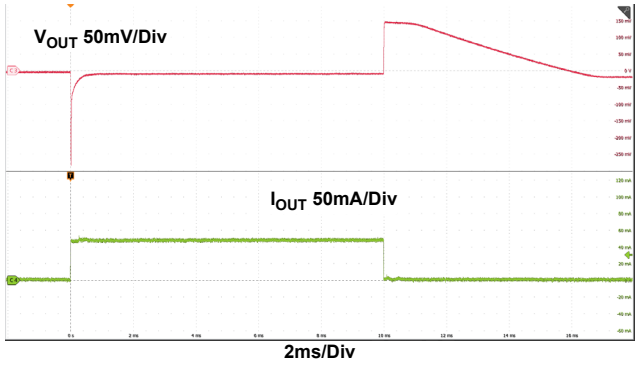


Figure 11. Load Transient Response (0 to 50mA)

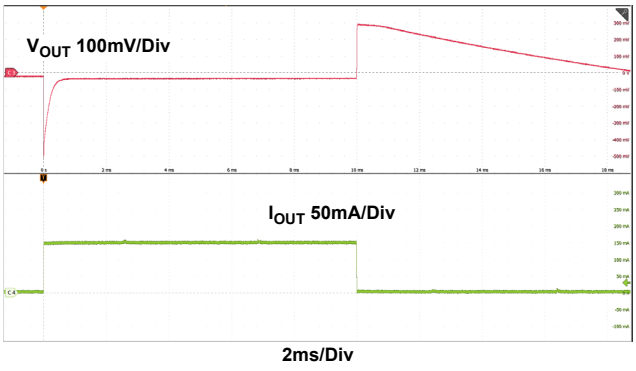


Figure 12. Load Transient Response (0 to 150mA)

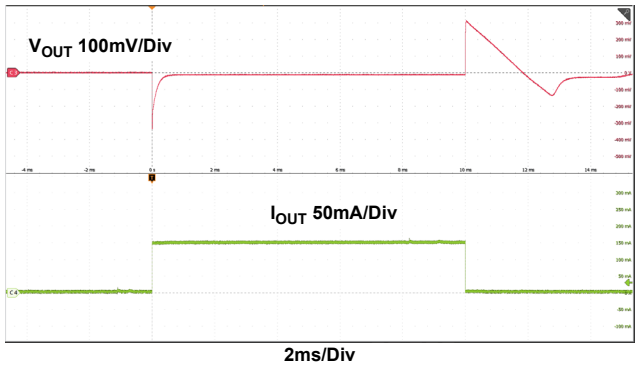


Figure 13. Load Transient Response (1mA to 150mA)

Unless otherwise specified, operating conditions are at: T = +25°C; V_{IN} = 6V, C_{OUT} = 2.2µF (Cont.)

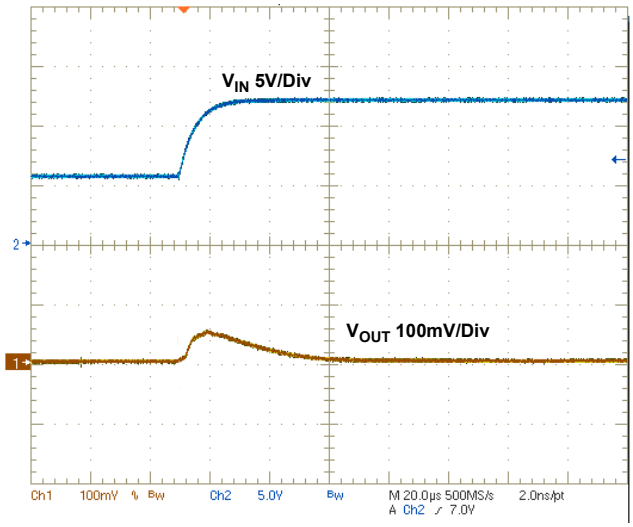


Figure 14. 6V to 12V Line Transient Response (I_{OUT} = 10mA)

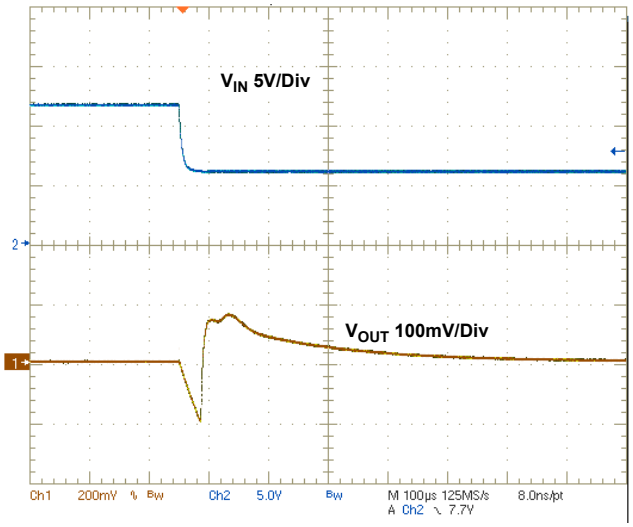


Figure 15. 12V to 6V Line Transient Response (I_{OUT} = 10mA)

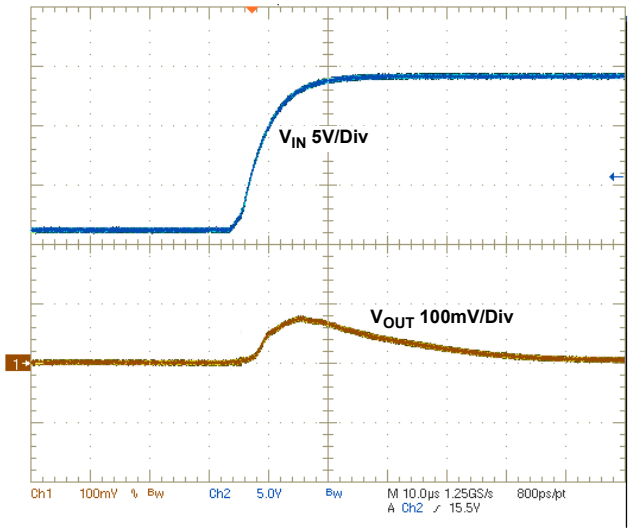


Figure 16. 12V to 24V Line Transient Response (I_{OUT} = 10mA)

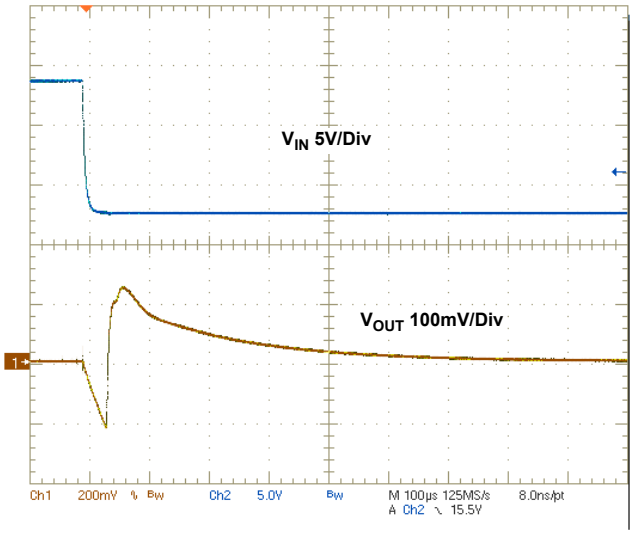


Figure 17. 24V to 12V Line Transient Response (I_{OUT} = 10mA)

4. Function Description

4.1 Function Overview

The RAA214401 is a high performance low dropout linear voltage regulator with 150mA sourcing capability. It operates at a wide input voltage range of 4.5V to 40V and regulates a precise and stable output voltage at 3.3V. With an ultra low quiescent current, it is an ideal choice for always-on applications. It works well under a load dump condition in which the input voltage could rise up to 40V.

4.2 Current Limit Protection

The RAA214401 has internal current limiting functionality to protect the regulator during fault conditions. During current limit, the output sources a fixed amount of current largely independent of the output voltage. If the short or overload is removed from V_{OUT}, the output returns to normal voltage regulation mode.

4.3 Over-Temperature Shutdown (OTSD)

If the die temperature exceeds the over-temperature threshold of the device, the output of the LDO shuts down until the die temperature cools down to a temperature determined by the hysteresis of the OTSD. The level of power dissipated, combined with the ambient temperature and the thermal impedance of the package, determines if the junction temperature exceeds the OTSD temperature.

4.4 Transient Response

The RAA214401 has excellent transient response to a load or line step. It can withstand a rapid change in load from zero to full load with minimal output capacitor required. It is also stable against fast transitions in the supply voltage, making it well suited for applications where two supplies are OR'ed into the regulator.

5. Application Information

5.1 Input and Output Capacitors

Renesas recommends connecting a 1µF ceramic capacitor between VIN and GND at the input to reduce circuit sensitivity to the Printed Circuit Board (PCB) layout. As a minimum, place a 0.1µF ceramic capacitor at the input for proper operation. Higher capacitance improves the line transient response.

The device is stable with an output ceramic capacitor in the range of 2.2µF to 200µF. Higher capacitance can help improve line transient response and reduce noise.

For both the input and output capacitors, the X7R type is recommended because it has low capacitance variation across temperature.

5.2 Power Dissipation

The junction temperature must not exceed the range specified in Recommended Operating Conditions. The power dissipation is calculated using Equation 1.

(EQ. 1) $$P_D = (V_{IN} - V_{OUT}) \times I_{OUT} + V_{IN} \times I_{GND}$$

To calculated the maximum ambient operating temperature, use the junction-to-ambient thermal resistance (θ_{JA}) as shown in Equation 2, where $T_{J(MAX)}$ is the maximum allowable junction temperature, and T_A is the ambient temperature.

(EQ. 2) $$T_{J(MAX)} = P_{D(MAX)} \times \theta_{JA} + T_A$$

For any target junction temperature, the maximum load the IC allows decreases as the supply voltage increases. Given the thermal resistance θ_{JA}, the Equation 1 and Equation 2 can be used to estimate the maximum load the IC supports up to its maximum junction temperature. The lower θ_{JA} is, the more load the device can handle. To lower, apply large trace metal area and ground plane on the PCB. For example, the θ_{JA} for this device measured on a PCB with 0.1 inch^2 ground trace area using 2oz copper is around 74°C/W. If the target maximum junction temperature is 125°C, Figure 18 shows the maximum load allowed on this PCB across the input voltage range at the ambient temperature of 25°C, 55°C, and 85°C, respectively.

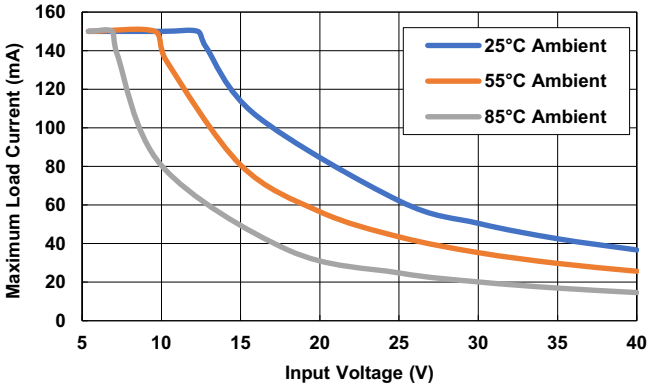


Figure 18. Maximum Allowable Load vs Input Voltage

5.3 Minimum Supply Voltage

The RAA214401 operates over a wide input voltage range up to 40V. Depending on the load current, the recommended minimum supply voltage can be found in Figure 19.

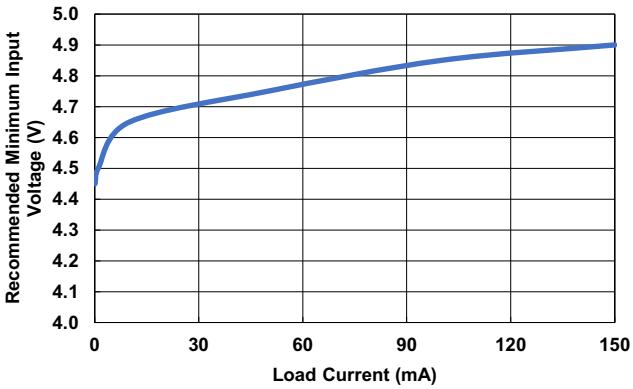


Figure 19. Recommended Minimum Input Voltage (V)

5.4 PCB Layout Recommendations

When placing components and routing the trace, minimize the ground impedance and keep the parasitic inductance low. Place the input and output capacitors as close to the IC as possible. VIN, VOUT, and GND traces should be reasonably wide to improve the thermal performance of the IC and to reduce the chance of noise pickup.

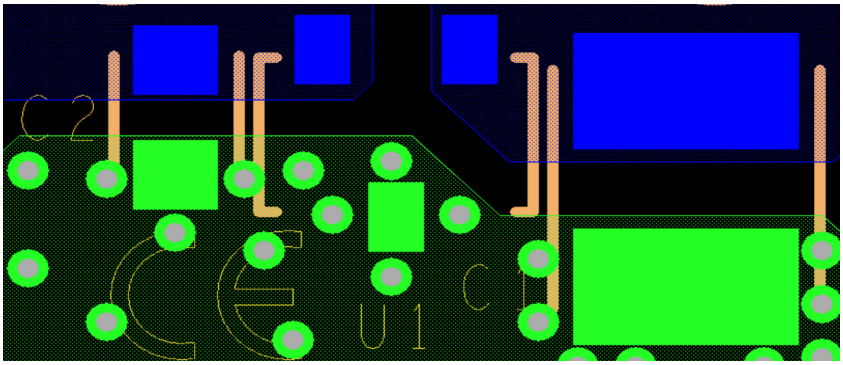
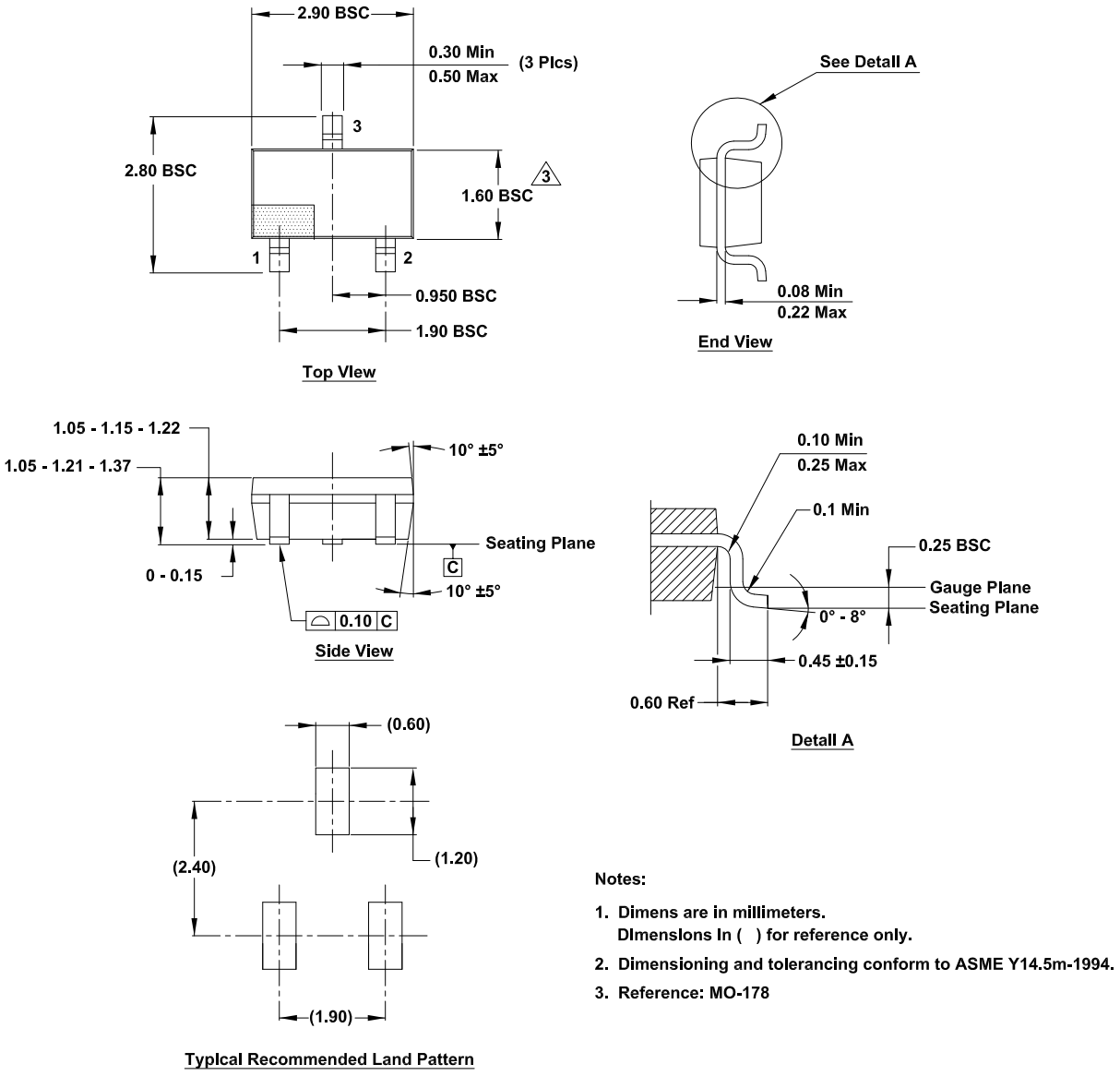


Figure 20. Recommended Capacitor Placement

6. Package Outline Drawing

For the most recent package outline drawing, see P3.064B.

P3.064B
3 Lead Small Outline Transistor Plastic Package (SOT23-3)
Rev 1, 3/2021



Notes:

1. Dimens are in millimeters.
   Dimensions In ( ) for reference only.
2. Dimensioning and tolerancing conform to ASME Y14.5m-1994.
3. Reference: MO-178

7. Ordering Information

Part Number [1][2]	Part Marking[3]	Package Description (RoHS Compliant)	Pkg. Dwg. #	Carrier Type[4]	Temp. Range
RAA2144014GP3#NA0	401	3 Ld SOT23	P3.064B	Reel, 250	-40°C to 125°C
RAA2144014GP3#JA0				Reel, 3k	
RTKA214401DR0000BU	Evaluation Board				

1. These Pb-free plastic packaged products employ special Pb-free material sets, molding compounds/die attach materials, and 100% matte tin plate plus anneal (e3 termination finish, which is RoHS compliant and compatible with both SnPb and Pb-free soldering operations). Pb-free products are MSL classified at Pb-free peak reflow temperatures that meet or exceed the Pb-free requirements of IPC/JEDEC J STD-020.
2. For Moisture Sensitivity Level (MSL), see the RAA214401 device page. For more information about MSL, see TB363.
3. The part marking is located on the bottom of the part.
4. See TB347 for details about reel specifications.

8. Revision History

Rev	Date	Description
2.01	Oct 6, 2022	Updated Figure 17.
2.00	Jul 22, 2022	Updated file number. Updated Line Regulation and Load Regulation specifications by swapping values. Updated POD P3.064B to the latest revision, changes are as follows: ▪ Updated Total Pkg Height from 1.45 Max to 1.05 - 1.21 - 1.37 ▪ Updated Standoff Height from 0.15 Max to 0-0.15 ▪ Updated Body Thickness from 1.15 +0.15/-0.25 to 1.05 - 1.15 - 1.22
1.01	Jun 7, 2021	Updated Line Regulation maximum specification from 0.004 to 0.006.
1.00	Mar 3, 2021	Initial release

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